FIG. 1

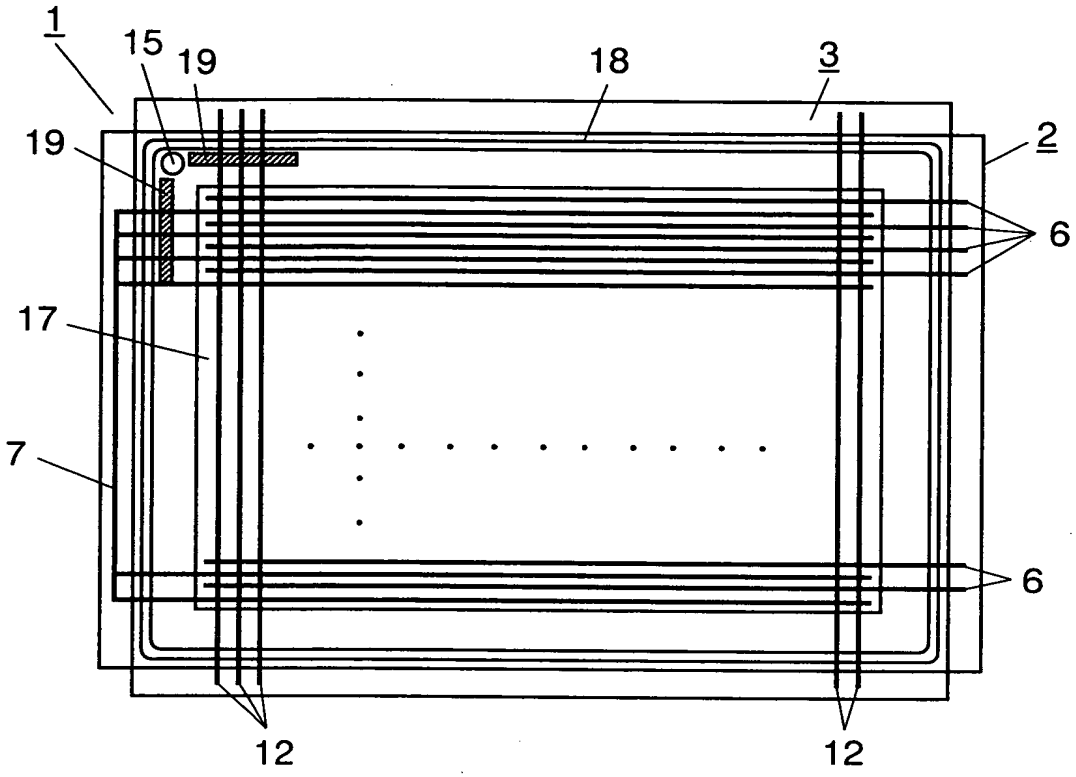


FIG. 2

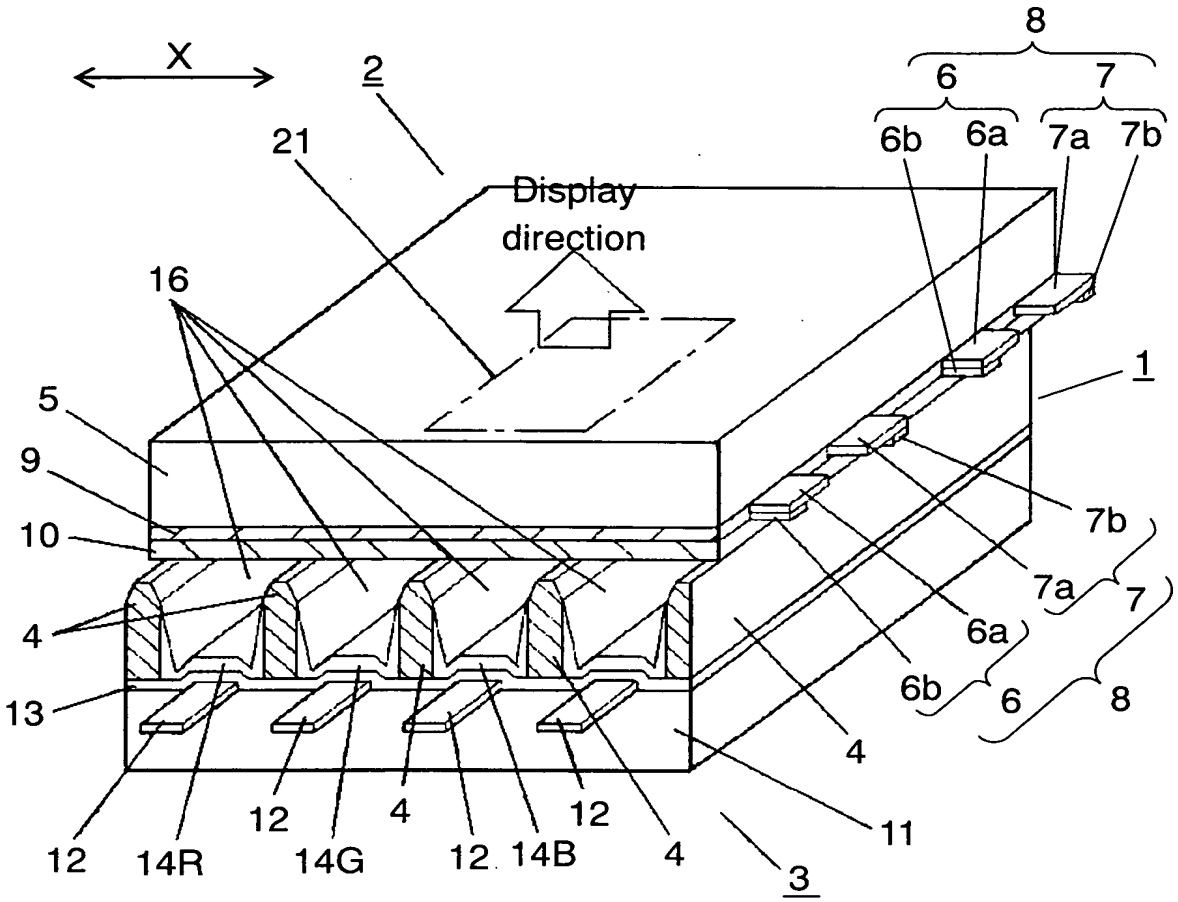

X
2
21
8
6
7
6b
6a
7a
7b
Display direction
16
1
5
9
10
4
13
12
14R
14G
14B
3
11

FIG. 3

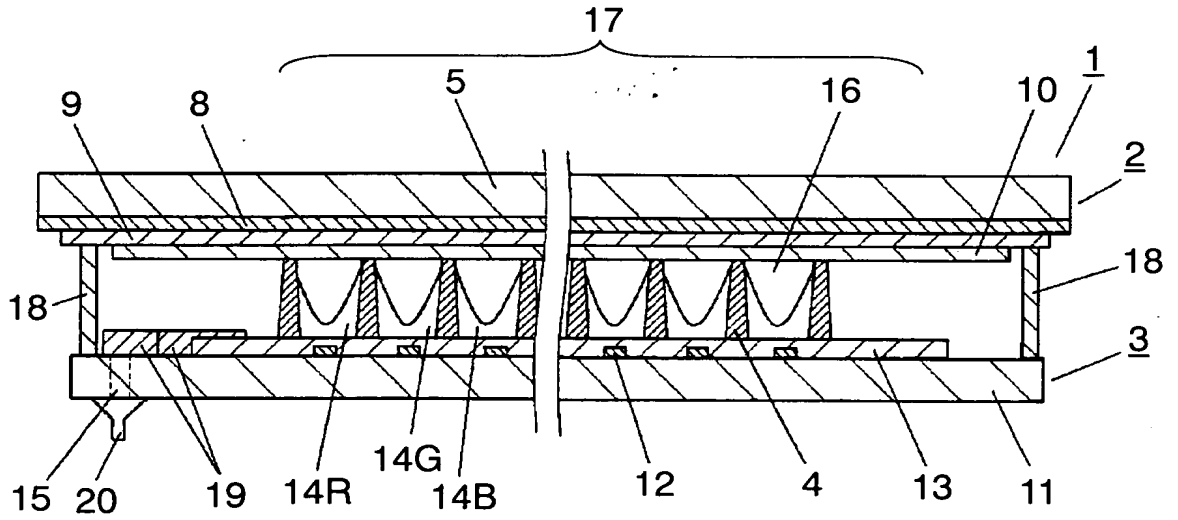

17
5
16
10
1
9
8
2
18
3
15
20
19
14R
14G
14B
12
4
13
11

FIG. 4

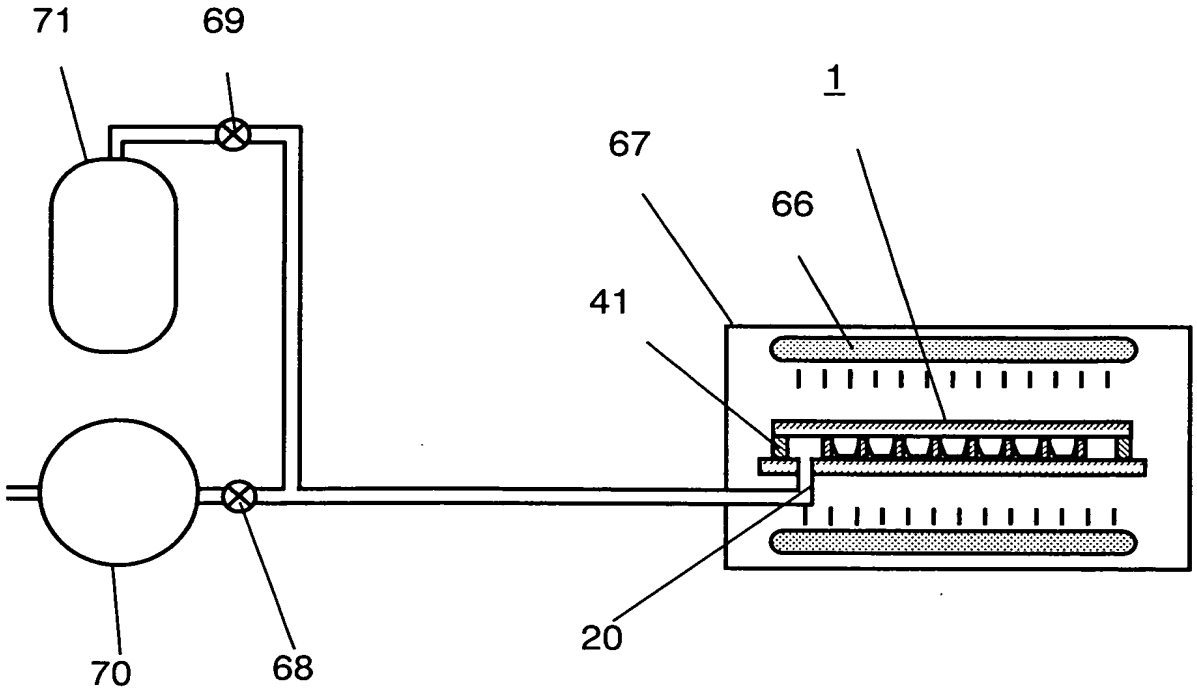


FIG. 5

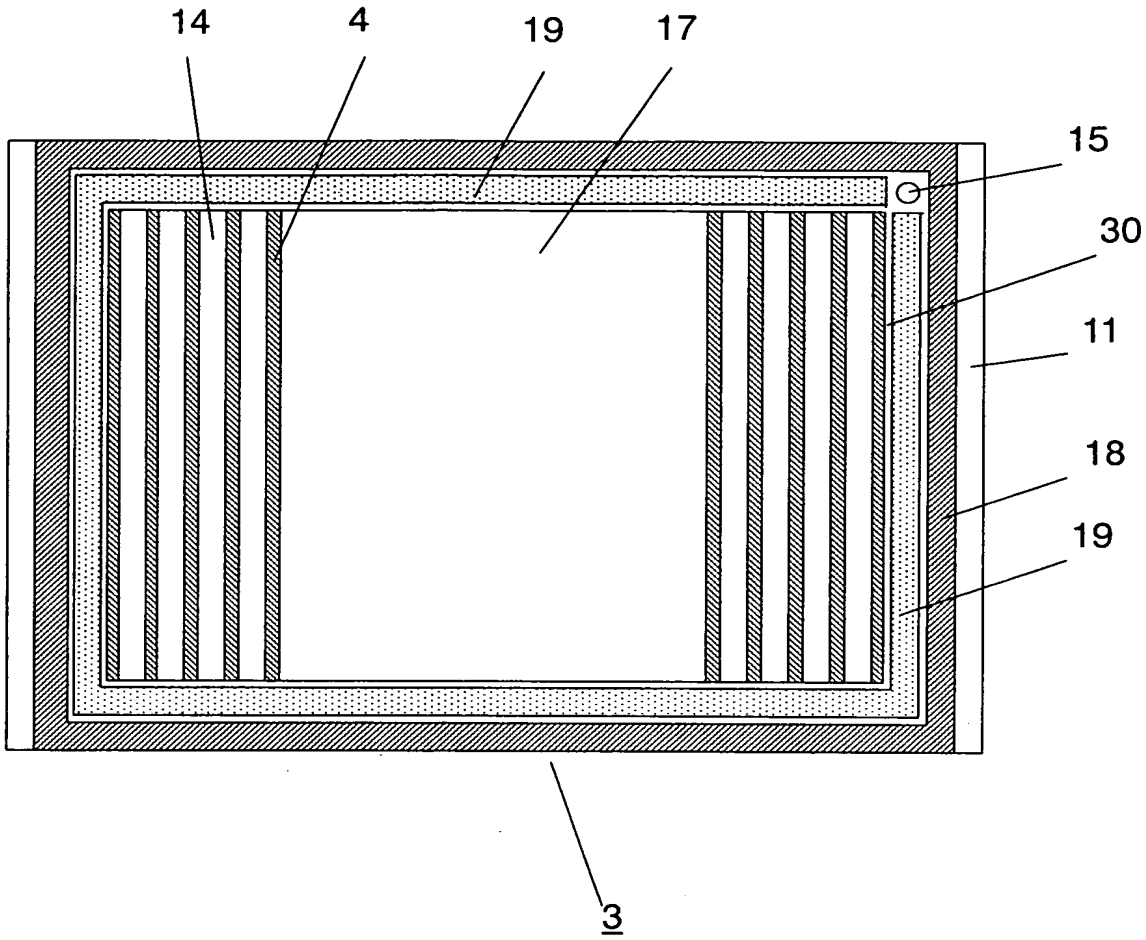
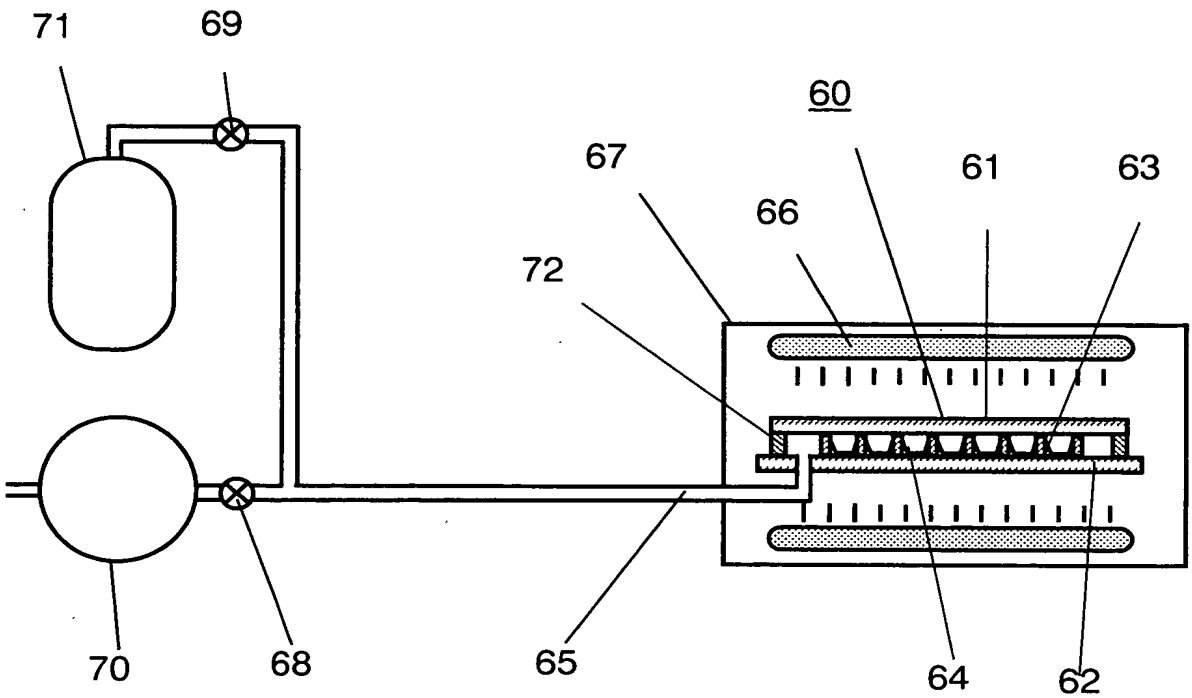


FIG. 6



Reference marks in the drawings

| | |
|---|---|
| 1 | PDP |
| 2 | Front board |
| 3 | Rear board |
| 4 | Partition |
| 5 | Front glass substrate |
| 6 | Scanning electrode |
| 7 | Maintenance electrode |
| 8 | Display electrode |
| 9 | Dielectric layer |
| 11 | Rear glass substrate |
| 12 | Data electrode |
| 16 | Dielectric layer |
| 14R | Red phosphor layer |
| 14G | Green phosphor layer |
| 14B | Blue phosphor layer |
| 15 | Exhaust hole |
| 16 | Discharge space |
| 17 | Image display area |
| 18 | Sealing member |
| 19 | Getter |
| 21 | Discharge cell |
| 30 | Non-image display area |